

### Power Supply Voltage

$\pm 12.0V \pm 1.0V$

$+5.0V \pm 0.5V$

150 mA Max.

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### Signal I/O Description

#### Video Input Signal (TTL)

Beam-On = TTL Logic level high

Beam-Off = TTL Logic level low

#### Output Power Control Signal Input

0 to -6.0 Vdc @ 5 milliamps Max. with a linear output power scaling factor to provide at least the output control range specified in Paragraph 4.1.1.1 (0.17 mW to 1.55 mW).

#### Output Power Level Signal

In the ON state, a dc voltage calibrated to 1.0 V/mW  $\pm 10\%$  when read with an instrument having an input impedance of 1 megohm or greater, shall be provided.

#### Signal Source

The video signal source shall be a 7438 open collector TTL buffer with 120 ohm load to +5V.

## Connector Terminal Assignments

<u>Terminal</u>	<u>Function</u>	<u>Remarks</u>
1	Dummy Plug	No connection
2	+12V DC	-
3	Power Return	IOU Power Ground Return
4	-12V DC	-
5	Output Level Status Signal	1 Volt/Milliwatt
6	+5V DC	-
7	Video Signal Return	IOU Signal Gnd. Near Video Input
8	Video Signal Input	-
9	Dummy Plug	No Connection at IOU
10	Output Level Control Signal	0 to -6 VDC

### Wiring Diagram

